

Notic of References CitedApplication/Control No.
09/437,135Applicant(s)/Patent Under
Reexamination
YAMAZAKI ET AL.Examiner
Erik KielinArt Unit
2813

Page 1 of 1

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	C	US-5,274,279	12-1993	Misawa et al.	326/103
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NON-PATENT DOCUMENTS

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	U	Wolf, et al. Silicon Processing for the VLSI Era, Vol. 1-Process Technology, Lattice Press: Sunset Beach CA, 1986, pp. 183-184.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.